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1 A graph formulation of a school scheduling algorithm

A. Salazar, R. V. Oakford

December 1974 **Communications of the ACM** , Volume 17 Issue 12

Publisher: ACM Request Permissions

Full text available: Pdf (262.62 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 3, Downloads (12 Months): 45, Downloads (Overall): 336,

The problem classically titled "The Examination Schedule Problem" takes various forms ir these formulations can be presented in the terminology of classical Network Theory. One Given a nondirected ...

Keywords: examination scheduling, graph, nondirected network, scheduling, school sch connected subgraph, subgraph

2 Scalable subgraph mapping for acyclic computation accelerators

Nathan Clark, Amir Hormati, Scott Mahlke, Sami Yehia

October 2006 **CASES '06:** Proceedings of the 2006 international conference on Compilers, ai synthesis for embedded systems

Publisher: ACM

Full text available: Pdf (906.08 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 36, Downloads (Overall): 182,

Computer architects are constantly faced with the need to improve performance and incr computation in their designs. To this end, it is increasingly common to see acyclic com-p appear in embedded processor designs. ...

Keywords: compilation, embedded processors

3 Revisiting pipelined parallelism in multi-join query processing

Bin Liu, Elke A. Rundensteiner

August 2005 **VLDB '05:** Proceedings of the 31st international conference on Very large data

Publisher: VLDB Endowment

Full text available: Pdf (304.67 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 45, Downloads (Overall): 276,

Multi-join queries are the core of any integration service that integrates data from multip sources. Due to the large number of data sources and possibly high volumes of data, the queries faces increasing scalability ...

4

Clone detection in automotive model-based development
Florian Deissenboeck, Benjamin Hummel, Elmar Jürgens, Bernhard Schätz, Stefan Wagner, Stefan Teuchert
May 2008 **ICSE '08:** Proceedings of the 30th international conference on Software engine
Publisher: ACM
Full text available: Pdf (308.99 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 14, Downloads (12 Months): 115, Downloads (Overall): 293

Model-based development is becoming an increasingly common development methodolo domains like embedded systems already major parts of the code are generated from mo domain-specific modelling languages. Hence, such models ...

Keywords: clone detection, data-flow, matlab/simulink, model clone

5 Load balancing and orientability thresholds for random hypergraphs
Pu Gao, Nicholas C. Wormald
June 2010 **STOC '10:** Proceedings of the 42nd ACM symposium on Theory of computing
Publisher: ACM Request Permissions
Full text available: Pdf (537.39 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 20, Downloads (12 Months): 20, Downloads (Overall): 20,

Let $h>w>0$ be two fixed integers. Let H be a random hypergraph whose hyperedges are w-orient a hyperedge, we assign exactly w of its vertices positive signs with respect to th rest negative. A ...
Keywords: cores, hypergraph orientation, load balancing, orientability thresholds

6 Component based channel assignment in single radio, multi-channel ad hoc networ
Ramanuja Vedantham, Sandeep Kakumanu, Sriram Lakshmanan, Raghupathy Sivakumar
September 2006 **MobiCom '06:** Proceedings of the 12th annual international conference on networking
Publisher: ACM Request Permissions
Full text available: Pdf (374.10 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 111, Downloads (Overall): 116

In this paper, we consider the channel assignment problem in single radio multi-channel networks. Specifically, we investigate the *granularity of channel assignment decisions* tha off in terms of performance ...

Keywords: channel assignment, multichannel routing, wireless ad hoc networks

7 Making mechatronic agents resource-aware in order to enable safe dynamic resourc
Sven Burmester, Matthias Gehrke, Holger Giese, Simon Oberthür
September 2004 **EMSOFT '04:** Proceedings of the 4th ACM international conference on Embe
Publisher: ACM Request Permissions
Full text available: Pdf (546.53 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 1, Downloads (12 Months): 15, Downloads (Overall): 245,

Mechatronic systems are embedded software systems with hard real-time requirements. paramount importance for these systems. Thus, their design has to take the worst-case i maximal required resources are usually ...

Keywords: dynamic resource allocation, real-time systems, resource awareness

8 MaxMin allocation via degree lower-bounded arborescences

MohammadHossein Bateni, Moses Charikar, Venkatesan Guruswami

May 2009 **STOC '09:** Proceedings of the 41st annual ACM symposium on Theory of compı

Publisher: ACM Request Permissions

Full text available: Pdf (483.66 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 90, Downloads (Overall): 107,

We consider the problem of MaxMin allocation of indivisible goods. There are m items to players. Each player i has a nonnegative valuation p_{ij} for an item j, and the goal is to a so as to maximize ...

Keywords: approximation algorithms, graphs, lift-and-project, linear programming

9 Proof verification and the hardness of approximation problems

Sanjeev Arora, Carsten Lund, Rajeev Motwani, Madhu Sudan, Mario Szegedy

May 1998 **Journal of the ACM (JACM)** , Volume 45 Issue 3

Publisher: ACM Request Permissions

Full text available: Pdf (418.87 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 37, Downloads (12 Months): 228, Downloads (Overall): 156

We show that every language in NP has a probablistic verifier that checks membership pı logarithmic number of random bits and by examining a constant number of bits in the pr language, then ...

Keywords: NP-completeness, optimization, proof verification, randomness

10 Minimum-weight triangulation is NP-hard

Wolfgang Mulzer, Günter Rote

May 2008 **Journal of the ACM (JACM)** , Volume 55 Issue 2

Publisher: ACM Request Permissions

Full text available: Pdf (723.54 KB) Additional Information: full citation, appendices and supplemer cited by, index terms

Bibliometrics: Downloads (6 Weeks): 20, Downloads (12 Months): 164, Downloads (Overall): 579

A triangulation of a planar point set *S* is a maximal plane straight-line graph with vertex *weight triangulation* (MWT) problem, we are looking for a triangulation of a given point s sum of ...

Keywords: Optimal triangulations, PLANAR 1-IN-3-SAT

11 Efficient scheduling of conditional behaviors for high-level synthesis

Apostolos A. Kountouris, Christophe Wolinski

July 2002 **Transactions on Design Automation of Electronic Systems (TODAES)** , V

Publisher: ACM Request Permissions

Full text available: Pdf (1.50 MB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 45, Downloads (Overall): 667,

As hardware designs get increasingly complex and time-to-market constraints get tighter motivation for high-level synthesis (HLS). HLS must efficiently handle both dataflow-dom dominated designs as well as designs ...

Keywords: Design automation, conditional behavior, high level synthesis (HLS), schedu

12 Space-efficient scheduling of nested parallelism

Girija J. Narlikar, Guy E. Blelloch

January 1999 **Transactions on Programming Languages and Systems (TOPLAS)** , Volun

Publisher: ACM Request Permissions

Full text available: Pdf (481.02 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 2, Downloads (12 Months): 41, Downloads (Overall): 368,

Many of today's high-level parallel languages support dynamic, fine-grained parallelism. the user to expose all the parallelism in the program, which is typically of a much higher number of processors. Hence an ...

Keywords: dynamic scheduling, multithreading, nested parallelism, parallel language im efficiency

13 A scheduling algorithm for optimization and early planning in high-level synthesis

Seda Ogrenci Memik, Ryan Kastner, Elaheh Bozorgzadeh, Majid Sarrafzadeh

January 2005 **Transactions on Design Automation of Electronic Systems (TODAES)** , \

Publisher: ACM Request Permissions

Full text available: Pdf (235.21 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 61, Downloads (Overall): 745,

Complexities of applications implemented on embedded and programmable systems grov capacities and capabilities of these systems. Mapping applications onto them manually is tedious task. This draws attention to using ...

Keywords: Scheduling, bipartite matching, data flow graph, high-level synthesis

14 Optimal packet scheduling in output-buffered optical switches with limited-range wav

Lin Liu, Yuanyuan Yang

December 2007 **ANCS '07:** Proceedings of the 3rd ACM/IEEE Symposium on Architecture for communications systems

Publisher: ACM Request Permissions

Full text available: Pdf (243.31 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 0, Downloads (12 Months): 29, Downloads (Overall): 101,

All-optical packet switching is a promising candidate for future high-speed switching. Hov absence of optical Ran-dom Access Memory, the traditional Virtual Output Queue (VOQ) switches are difficult to implement in ...

Keywords: WDM optical switches, minimum cost maximum flow, output-queued (OQ), p wavelength conversion

15 An Architecture Framework for Transparent Instruction Set Customization in Embed

Nathan Clark, Jason Blome, Michael Chu, Scott Mahlke, Stuart Biles, Krisztian Flautner

June 2005 **ISCA '05:** Proceedings of the 32nd annual international symposium on Comput
Publisher: ACM
Full text available: Pdf (379.01 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 84, Downloads (Overall): 522,

Instruction set customization is an effective way to improve processor performance. Criti applicationdata-flow graphs are collapsed for accelerated execution on specialized hardw subgraphs will compress the latency ...

Also published in:
May 2005 **SIGARCH Computer Architecture News** Volume 33 Issue 2

16 A general framework for prefetch scheduling in linked data structures and its applica prefetching
Seungryul Choi, Nicholas Kohout, Sumit Pamnani, Dongkeun Kim, Donald Yeung
May 2004 **Transactions on Computer Systems (TOCS)** , Volume 22 Issue 2
Publisher: ACM Request Permissions
Full text available: Pdf (2.45 MB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 89, Downloads (Overall): 1389,

Pointer-chasing applications tend to traverse composite data structures consisting of mul pointer chains. While the traversal of any single pointer chain leads to the serialization of the traversal of independent pointer ...

Keywords: Data prefetching, memory parallelism, pointer-chasing code

17 Topology control meets SINR: the scheduling complexity of arbitrary topologies
Thomas Moscibroda, Roger Wattenhofer, Aaron Zollinger
May 2006 **MobiHoc '06:** Proceedings of the 7th ACM international symposium on Mobile computing
Publisher: ACM Request Permissions
Full text available: Pdf (346.52 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 14, Downloads (12 Months): 109, Downloads (Overall): 744

To date, topology control in wireless ad hoc and sensor networks--the study of how to co communication network a subgraph with certain beneficial properties .has been consider only; the time required to actually ...

Keywords: algorithmic analysis, interference, scheduling complexity, topology control, v

18 Efficient interference-aware TDMA link scheduling for static wireless networks
Weizhao Wang, Yu Wang, Xiang-Yang Li, Wen-Zhan Song, Ophir Frieder
September 2006 **MobiCom '06:** Proceedings of the 12th annual international conference on networking
Publisher: ACM Request Permissions
Full text available: Pdf (305.63 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 11, Downloads (12 Months): 140, Downloads (Overall): 108

We study efficient *link scheduling* for a multihop wireless network to maximize its throug scheduling can greatly reduce the interference effect of close-by transmissions. Unlike th often assume a unit ...

Keywords: distributed algorithm, graph coloring, interference, link scheduling, wireless

19 Improved bounds for scheduling conflicting jobs with minsum criteria

Rajiv Gandhi, Magnús M. Halldórsson, Guy Kortsarz, Hadas Shachnai

March 2008 **Transactions on Algorithms (TALG)** , Volume 4 Issue 1

Publisher: ACM Request Permissions

Full text available: Pdf (162.59 KB) Additional Information: full citation, abstract, references, index

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 57, Downloads (Overall): 218,

We consider a general class of scheduling problems where a set of *conflicting* jobs needs (preemptively or nonpreemptively) on a set of machines so as to minimize the weighted times. The conflicts among jobs are ...

Keywords: Approximation algorithms, LP rounding, coloring, linear programming, sched

20 Instruction scheduling for a tiled dataflow architecture

Martha Mercaldi, Steven Swanson, Andrew Petersen, Andrew Putnam, Andrew Schwerin, Ma Eggers

November 2006 **ASPLOS-XII:** Proceedings of the 12th international conference on Architecti programming languages and operating systems

Publisher: ACM Request Permissions

Full text available: Pdf (490.50 KB) Additional Information: full citation, abstract, references, cited

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 70, Downloads (Overall): 612,

This paper explores hierarchical instruction scheduling for a tiled processor. Our results s level of the hierarchy, a simple profile-driven algorithm effectively minimizes operand lat has been partitioned ...

Keywords: dataflow, instruction scheduling, tiled architectures

Also published in:

October 2006	**SIGOPS Operating Systems Review**	Volume 40 Issue 5
October 2006	**SIGARCH Computer Architecture News**	Volume 34 Issue 5
November 2006	**SIGPLAN Notices**	Volume 41 Issue 11

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